

(2)

Code : 031609

B.Tech 6th Semester Exam., 2018

POWER ELECTRONICS

Time : 3 hours

Full Marks : 70

Instructions :

- (i) The marks are indicated in the right-hand margin.
- (ii) There are **NINE** questions in this paper.
- (iii) Attempt **FIVE** questions in all.
- (iv) Question No. 1 is compulsory.

1. Choose the correct option (any seven) : $2 \times 7 = 14$ (a) The number of p - n junctions in a thyristor is

- (i) 1
- (ii) 2
- (iii) 3
- (iv) 4

(b) When a thyristor gets turned on, the gate drive

- (i) should not be removed as it will turn-off the SCR
- (ii) may or may not be removed
- (iii) should be removed
- (iv) should be removed to avoid increased losses and higher junction temperature

(c) Once SCR starts conducting a forward current, its gate loses control over

- (i) anode circuit voltage only
- (ii) anode circuit current only
- (iii) anode circuit voltage and current
- (iv) anode circuit voltage, current and time

(d) In a single-phase full converter, for continuous conduction, each pair of SCRs conduct for

- (i) $\pi - \alpha$
- (ii) π
- (iii) α
- (iv) $\pi + \alpha$

(e) Commutation overlap in the phase-controlled a.c. to d.c. converters is due to

- (i) load inductance
- (ii) harmonic content of the load
- (iii) switching operation in the converter
- (iv) source inductance

(f) A converter which can operate in both 3-pulse and 6-pulse modes is a

- (i) 1-phase full converter
- (ii) 3-phase half-wave converter
- (iii) 3-phase semi-converter
- (iv) 3-phase full converter

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- (g) In a 3-phase semi-converter, the three SCRs are triggered at an interval of
 (i) 60° (ii) 90°
 (iii) 120° (iv) 180°
- (h) In single-pulse modulation of PWM inverters, third harmonic can be eliminated if pulse width is equal to
 (i) 30° (ii) 60°
 (iii) 120° (iv) 150°
- (i) In d.c. choppers, the waveforms for input and output voltages are
 (i) respectively discontinuous, continuous
 (ii) both continuous
 (iii) both discontinuous
 (iv) respectively continuous, discontinuous
- (j) In a 1-phase to 1-phase step-down cycloconverter
 (i) all SCRs must carry currents for equal duration
 (ii) the duration of currents in all SCRs cannot be equal
 (iii) the duration of currents in all the SCRs may or may not be equal
 (iv) all SCRs must carry currents for equal duration for dependable operation

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3. (a) Explain the two-transistor model of a thyristor. 6
 (b) Sketch switching (or dynamic) characteristics of a thyristor during its turn-on and turn-off processes. Show the variation of voltage across the thyristor and current through it during these two dynamic processes. 8
3. Describe a 1-phase full-wave diode-bridge rectifier with a circuit diagram and relevant waveforms for load R . Hence, derive expressions for average and r.m.s. values of output voltage and obtain therefrom voltage ripple factor (VRF), form factor (FF) and rectifier efficiency. 14
4. (a) Enumerate the various techniques by which thyristors can be triggered into conduction. 6
 (b) Explain the working principle of snubber circuit used for protection of thyristor. 8
5. (a) Discuss the principle of working of a three-phase inverter with an appropriate circuit diagram. Draw phase and line voltage waveforms on the assumption that each thyristor conducts for 120° and the resistive load is star-connected. 8

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- (b) A three-phase bridge inverter delivers power to a resistive load from a 450 V d.c. source. For a star-connected load of $10\ \Omega$ per phase with 120° mode of operation, calculate the following :

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- (i) r.m.s. value of load current
(ii) r.m.s. value of thyristor current
(iii) Load power

- 6/ (a) What is meant by step-up chopper? Explain its operation. Sketch the input voltage, input current, output voltage and output current waveforms. State the various assumptions made.

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- (b) A d.c. chopper is fed from 100 V d.c. Its load voltage consists of rectangular pulses of duration 1 m sec in an overall cycle time of 3 m sec. Find the average output voltage and ripple factor for this chopper.

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7. (a) What is an a.c. voltage controller? List some of its industrial applications. Enumerate its merits and demerits.

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- (b) A 3-phase half-wave controlled converter is fed from 3-phase, 400 V, 50 Hz source and is connected to load taking a constant current of 36 A. Thyristors have a voltage drop of 1.4 V. Find out the average value of load voltage for firing angle of 30° and 60° .

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(Turn Over)

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8. Describe the modified McMurray-Bedford half-bridge single-phase inverter with relevant voltage and current waveforms. The working of this inverter may be explained in certain well-defined modes. Enumerate the simplifying assumptions made.

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9. A single-phase bridge-type cycloconverter has input voltage of 230 V, 50 Hz and load of $R = 10\ \Omega$. Output frequency is one-third of input frequency. For a firing angle delay of 30° , calculate (a) r.m.s. value of output voltage, (b) r.m.s. current of each converter, (c) r.m.s. current of each thyristor and (d) input power factor.

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