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B.Tech 5th Semester Exam., 2014

MICROPROCESSOR AND ITS APPLICATION

Time: 3 hours

Full Marks: 70

Instructions:

- (i) The marks are indicated in the right-hand margin.
- (ii) There are MINE questions in this paper.
- (iii) Attempt FIVE questions in all.
- (iv) Question No. 1 is compulsory.
- 1. Choose the correct option (any seven): 2×7=14
 - (a) In Intel 8085 microprocessor, number of status flags is
 - . (i) 3
 - *_(ii*) 5
 - (iii) 7
 - (iv) 9
 - The number of memory locations which can be directly addressed by Intel 8085 microprocessor is
 - (i) 28
 - (ii) 2¹²
 - *ூய்*) 2¹⁶
 - (iv) 2^{20}

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In the instruction STA, 2400 H, the mode of addressing is

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- direct addressing
- (ii) register addressing
- (iii) register indirect addressing
- (iv) immediate addressing
- (d) When LHLD, 2500 H instruction is executed, the flag affected is
 - (i) zero flag
 - (ii) sign flag
 - (iii) parity and sign flag
 - (iv) None of the above
- (e) In Intel 8255, 98 H is used as control word. Which of the following statements is correct?
 - (i) Port A is used as input port
 - (ii) Port B is used as input port
 - (iii) Port Clower is used as input port
 - (iv) None of the above
- Intel 8253 is a
 - communication (i) programmable interface
 - (ii) programmable interval timer
 - (jii) programmable DMA controller
 - (iv) None of the above

(g) The width of data bus in Intel 8086 microprocessor is (i) 8-bit (ii) 12-bit (iii) 16-bit (iv) 20-bit	(i) ADC is used to (i) control and (ii) control and (iii) convert and device (iv) convert and
 (h) In maximum mode of operation of Intel 8086 microprocessor, the signals S₂, S₁ and S₀ have the status 0, 0, 1 respectively. Which of the following operations will be performed? (i) Interrupt acknowledge (ii) Read data from I/O port (iii) Write data into I/O port 	signal 2. (a) What is the ficontrol unit of Discuss. (b) What is the roll microprocessor? various status microprocessor.
(iv) Halt (i) In Intel 8086 microprocessor, when the instruction MOV[0301], AX is executed	3. (a) Write an assemb find the smallest array for 8085 n
(i) register AX is loaded with 0301 H (ii) the content of memory location 0301 is transferred to the register AX	(b) Write an assemble add two 16-bit no be 16-bit or more processor.
(iii) the content of register AX is	4 (a) Discourse

0 alog and digital devices alog and digital signals nalog device to digital nalog signal to digital • unction of timing and 8085 microprocessor? 6 le of status flags in a Discuss the function of flags of 8085 8 ly language program to t number from a data nicroprocessor. 7 ly language pregram to umbers, the sum may ore for 8085 micro-7 Discuss the direct memory access data transfer scheme. 7 What is handshaking mode of data transfer? Discuss, 7

0301

(iv) None of the above

transferred to the memory location

5. What is Intel 8253? Discuss how this chip is used to generate delay and square wave.	14
6. (a) How many machine cycles are required by the following instructions of Intel 8085?	6
(i) MOV r₁, r₂ (ii) MVI r, data (iii) MOV r, M	
(iv) LXI rp, data	
(v) LDA addr	
(vi) IN addr	
(b) Draw the timing diagram for LXI H, 2400 H instruction.	8
7. (a) What are the roles of pointer and index registers in Intel 8086 microprocessor? How are they used?	7
(b) How is the physical address generated in 8086 microprocessor? Explain with examples.	7
 (a) Explain the following instructions of Intel 8086 microprocessor: 	f 6
(i) MUL mem/reg	
(ii) IDIV mem/reg	
(iii) LOOPNZ disp	
(iv) MOVSW	

(b) Write an assembly language program to get the sum of series of 16-bit numbers for 8086 microprocessor.

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9. Write notes on any two of the following:

7×2=14

(a) Sample and hold

(b) Assembler

(c) Stack pointer

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